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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/722,520	11/28/2003	Takashi Noguchi	2003-1693A	7575
513 7	590 05/06/2005	EXAMINER		
	TH, LIND & PONAC	HUYNH, ANDY		
2033 K STREET N. W. SUITE 800			ART UNIT	PAPER NUMBER
	N, DC 20006-1021		2818	

DATE MAILED: 05/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	No	Applicant(s)	_		
Office Action Summary							
		10/722,520		NOGUCHI, TAKASHI			
	Office Action Summary	Examiner		Art Unit			
		Andy Huynh		2818			
Period for	The MAILING DATE of this communication app Reply	pears on the o	cover sheet with the c	orrespondence address			
THE M - Extens after S - If the p - If NO p - Failure Any re	RTENED STATUTORY PERIOD FOR REPLIALING DATE OF THIS COMMUNICATION. ions of time may be available under the provisions of 37 CFR 1.1 IX (6) MONTHS from the mailing date of this communication. eriod for reply specified above is less than thirty (30) days, a replication for reply is specified above, the maximum statutory period to reply within the set or extended period for reply will, by statute ply received by the Office later than three months after the mailing patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event bly within the statuto will apply and will o e, cause the applic	, however, may a reply be tim ry minimum of thirty (30) days expire SIX (6) MONTHS from the ation to become ABANDONED	ely filed will be considered timely. he mailing date of this communication. 0 (35 U.S.C. § 133).			
Status							
1)⊠ F	Responsive to communication(s) filed on 29 N	March 2005.					
· <u> </u>	This action is FINAL . 2b)⊠ This action is non-final.						
-	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositio	on of Claims						
5)⊠ (6)⊠ (7)⊠ (Claim(s) 1-14 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) 5-14 is/are allowed. Claim(s) 1 is/are rejected. Claim(s) 2-4 is/are objected to. Claim(s) are subject to restriction and/or election requirement.						
Application	on Papers						
10)⊠ T ,⁄ F	The specification is objected to by the Examinative drawing(s) filed on <u>28 November 2003</u> is/a Applicant may not request that any objection to the Replacement drawing sheet(s) including the corrective oath or declaration is objected to by the Example 2015.	are: a)⊠ acc e drawing(s) be ction is required	held in abeyance. See I if the drawing(s) is obj	37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority ur	nder 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
2) Notice 3) Inform	s) of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) No(s)/Mail Date 03/29/05.		4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:				

DETAILED ACTION

In the Amendment filed 03/29/2005, the specification, claims 1-3, 5-8 and 10-13 are amended, and claim 15 is canceled is acknowledged. Accordingly, claims 1-14 are currently pending in the application.

Response to Arguments

Applicant's arguments with respect to claim 1 have been considered but are moot in view of the new ground(s) of rejection.

Priority

Acknowledgment is made of applicant's claim for foreign priority based on an application filed in JAPAN on 11/28/2002. It is noted, however, that applicant has not filed a certified copy of the 345641/2002 application as required by 35 U.S.C. 119(b).

Information Disclosure Statement

This office acknowledges receipt of the following items from the applicant: Information Disclosure Statement (IDS) filed on 03/29/2005 and made of record as Paper No. 03/29/05. The references cited on the PTOL 1449 form have been considered.

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Claim Rejections - 35 U.S.C. § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Hakamata Kazuo (JP 10-242442 dated 09/11/1998).

Hakamata Kazuo discloses in Figs. 1-2 and the corresponding English translation texts as set forth in paragraphs [0023]-[0039], a semiconductor device comprises:

a semiconductor chip/an image pickup device 10 having a main surface provided with an integrated circuit including a photoelectric converter/an image pickup region 11;

a plurality of electrodes/bumps 60 formed in a vicinity of a periphery of the integrated circuit;

a sealing resin 70 for sealing the main surface of the semiconductor chip and at least a first side surface of each of the electrodes, the sealing resin formed so as to have an opening over the a surface of the integrated circuit; and

a light-transmitting cap/a transparent hard substrate 50 disposed so as to cover the opening of the sealing resin.

Claim 1 is also rejected under 35 U.S.C. 102(b) as being anticipated by Hasegawa et al. (JP 08-065579 dated 03/08/1996).

Hasegawa et al. disclose in Fig. 3 and the corresponding English translation texts as set forth in paragraphs [0015]-[0026], a semiconductor device/a solid state image pickup device comprises:

a semiconductor chip/a CCD chip 42 having a main surface provided with an integrated circuit including a photoelectric converter 43;

a plurality of electrodes/bumps 46 formed in a vicinity of a periphery of the integrated circuit;

a sealing resin 52 for sealing the main surface of the semiconductor chip/the CCD chip and at least a first side surface of each of the electrodes/the bumps, the sealing resin formed so as to have an opening/an air layer 50 over the a surface of the integrated circuit; and

a light-transmitting cap/a cover glass 49 disposed so as to cover the opening of the sealing resin.

Allowable Subject Matter

Claims 2-4 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 5-14 are allowed.

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Conclusion

A shortened statutory period for response to this action is set to expire 3 (three) months and 0 (zero) day from the day of this letter. Failure to respond within the period for response will cause the application to become abandoned (see M.P.E.P 710.02(b)).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andy Huynh, (571) 272-1781. The examiner can normally be reached on Monday-Friday from 8:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The Fax number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the -status of this application or proceeding should be directed to the receptionist whose phone number is (703) 308-0956.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ah

Andy Huynh

05/02/05

Patent Examiner

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